

# ABSTRACT

The present invention provides an overcurrent detection circuit capable of suppressing fluctuations in the overcurrent detection level when the input supply voltage or temperature fluctuates. The overcurrent detection circuit 10b comprises a monitor transistor 21 a control terminal and output terminal of which are connected to a control terminal and output terminal respectively of an output transistor 11; an output current detection transistor 22b to a control terminal of which a detection bias voltage 28 is input, and an output terminal of which is connected to an input terminal of the monitor transistor 21; a constant current source 24 that generates a reference current  $I_{REF}$ ; a reference transistor 23 to a control terminal of which the detection bias voltage 28 is input and from the output terminal of which the reference current  $I_{REF}$  flows to the constant current source 24; and a comparison circuit 25b that outputs an overcurrent detection signal by comparing the voltage of the output terminal of the output current detection transistor 22b and the voltage of the output terminal of the reference transistor 23.